
Jelly

Ryuz(<https://github.com/ryuz/>)

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Contents:

1	Overview	1
1.1	ãAřãAŸãĆAãAñ	1
1.2	æğÑæĹŘ	1
1.3	Real-time OS ãAñãAđ'ãAĎãAę	2
1.4	Real-time Neural Network ãAñãAđ'ãAĎãAę	2
1.5	JellyãĆŠă;£ãAčãA§ãĆũãĆzãĈEãĈăãAőãĈGãĈcãŦçŦz	2
2	DMA	3
2.1	jelly_buffer_manager	3
2.2	jelly_buffer_allocator	3
2.3	jelly_dma_stream_write	3
2.4	jelly_dma_stream_read	4
2.5	jelly_dma_fifo	5
2.6	jelly_dma_video_write	5
2.7	jelly_dma_video_read	6
3	Video éŮcéĂč	7
3.1	jelly_video_parameter_update	7
3.2	jelly_video_format_regularizer	8
3.3	jelly_vin_axi4s	8
3.4	jelly_vout_axi4s	8
3.5	jelly_vsync_generator	8
3.6	jelly_dvi_tx	8
3.7	jelly_mipi_csi2_rx	8
3.8	jelly_hdmi_rx	9
3.9	jelly_vdma_axi4s_to_axi4	9
3.10	jelly_vdma_axi4_to_axi4s	9
4	çŦzãĈŘãĈğçŘEçŮcéĂč	11
4.1	img ãĈŘãĈzãŦæğŸ	11
4.2	jelly_axi4s_to_img	11
4.3	jelly_img_blk_buffer	12
4.4	jelly_img_demosaic_acpi	12
4.5	jelly_img_color_matrix	12
4.6	jelly_img_gaussian_3x3	12
4.7	jelly_img_sobel_core	12
4.8	jelly_img_binarizer	12

4.9	jelly_img_selector	12
4.10	jelly_img_previous_frame	13
4.11	jelly_img_dnn_maxpol	13
5	Library éŮcéĂč	15
5.1	æĕCèĕA	15
5.2	âŔĎçlôâĈl'âĈd'âĈŮâĈl'âĈl	15
6	CPU (MIPS-I compatible)	17
6.1	æĕCèĕA	17
6.2	CPUâĈşâĈcâĈăAôæĕCèĕA	17
6.3	jelly_cpu_top âĈcâĈŷâĈčâĈijâĈñ	17
6.4	CPUâĈĜâĈŔâĈĈâĈŕâĈlâĈijâĈl	22
6.5	UARTçtŋçŦŝâĈĜâĈŔâĈĈâĈŕâĈlŮâç	23
6.6	WISHBONEâĈŔâĈz	24
6.7	Jelly bus	24
6.8	âŦjâzd'âĈzâĈĈâĈlç■l'	25
7	MIPS-I äžŖæŔŽăĈj;ăĈŦăĈlăĈşăĈćçŖăćĈ	27
7.1	ăAŕăAŸăĈAăAñ	27
7.2	Jelly-CPUăAôçl'žâçt'	29
7.3	ăĈûăĈzăĈĖăĈcăăAôĕhăĈŸŎ	29
7.4	CPUăĈşăĈcâĈăAôăĈŮăĈ■ăĈĈăĈŕăĈžş	29
7.5	éŮŇçŽçŖŖăćĈăAñăAđ'ăAĎăAĕ	30
8	Indices and tables	31

1.1 Introduction

Jelly is a high-level FPGA synthesis tool. It is designed to be easy to use and to generate high-quality code. It supports a wide range of target devices, including Xilinx, Altera, and Lattice. It also supports a variety of synthesis targets, including ASICs, FPGAs, and CPLDs. The tool is designed to be easy to use and to generate high-quality code. It supports a wide range of target devices, including Xilinx, Altera, and Lattice. It also supports a variety of synthesis targets, including ASICs, FPGAs, and CPLDs.

1.2 Getting Started

1.2.1 Installing Jelly

1.2.2 Running Jelly

1.2.3 Project Setup

After installing Jelly, you can run it from the command line. To create a new project, use the following command:

1.3 Real-time OS ãAñãAđ'ãAĐãAę

æŃŽä;IJãAõ HOS-V4a ãAñãAęãÄAäÿLèlŸãAõ MPISăžŠæRŽãCşãĆcãÄAµMicroBlazeãÄACortex-R5
ãAłãAł'ãAñãr;ã£IJäÿ■ãAğãAZãĂĆ

1.4 Real-time Neural Network ãAñãAđ'ãAĐãAę

ã;ôãLEãRfèÇ;ãZđeũfèlŸe£řãAñãşzãAęãARLUT çŽt' æŒã■ç£ŠãAñãĆLãĆNæũsãsd'ã■ç£ŠãĆcãČGãČñ(LUT-
Network)çTlãAôã■ç£ŠçŠřãćČãAłãAŮãAęéŮŃçŽžäÿ■ãAđBinaryBrainãĆĆJellyãAđçTzãČRãĞęçŘEãCşãČşãČlãČijãČ■ãČşãČLãČŞçT
AXI4-Stream ãAłãAđãAğæšŒçTlçŽĐãAğãAZ)ãĂĆ

ãČããČcãČlãČŠãzNãAŤãAŽãAñãČlãČcãČñãČ£ãČd'ãČããAñæŒlènŮçŤŘædIJãAŃãĞžãŁZãRfèÇ;ãAğãAĆãČLãÄAæIJñãČŮãČl'ãČČãČ

1.5 JellyãĆŠã;£ãAćãAşãĆũãĆzãČEãČããAłãČGãČcãŃŤçŤž

- Real-Time Deep Neural Network
- Real-Time GPU
- IMX219 1000fps
- IMX219+OLED

2.1 jelly_buffer_manager

DMA is a hardware component that can transfer data between memory and I/O devices without the intervention of the CPU. It is used to transfer data between the host and the device.

2.2 jelly_buffer_allocator

jelly_buffer_manager is a software component that manages the allocation and deallocation of buffers for DMA transfers. It is responsible for ensuring that the buffers are of the correct size and are located in the correct memory region.

2.3 jelly_dma_stream_write

2.3.1 æðèèA

NæñqáĚĊæġŇéĀāāŎ Stream āAŇāCL AXI4 āČqāČčāČlāČŘāČzāAŷāAĪāAŮāAęæŽŷāA■ē; ijāAĤāČŚēāŇāAĤāČ æŽŷāA■ē; ijāAĤāĤĒLāAŎāČŘāČČāČŤāČqāLŭā; qāAĪāĀAāČĜāČijāČĤāAŎæŽŷāA■ē; ijāAĤāAŧāAĪāČŚŇŇŋŇNæĀġénŸāAŘæL' sāAĎāā āĤĚēČĪāAŋFIFOāČŘāČČāČŤāČqāČŚæIJL' āAŮāAęāAĤāČLāĀAāČŘāČČāČŤāČqāAŋæžŮāČZāAġāA■āA§āČĤāČd' āČzāĤĤāAŎāAĤāy CPUāAŇāCL' āAŎāČŇāČŷāČzāČĤLŭæĒŇād' L' āŇŮāČĎāL' sāČLē; ijāAĤāAĤāĀāĤĚēČĪFIFOāAŎæŏŇéĜŘāAŋçĎāēŮēāĤČāAŋAXI4ā NæñqáĚĊæġŇéĀāāČŚāŘĎæñqáĚĊāŎ first āAĪ last āAġēāĪāAŽāĀČČL' zāAŋ last āČŚāĤĤāAŮāAę āŷāČĜāČijāČĤāČĤāČŚāČĜāČČāČŚāČŘāAŮāA§āČLāĀAā; ŽāĤĤāČĜāČijāČĤāČŚāČŇāČČāČLāAŮāA§āČLāAġāA■āČŇāĀČāA; āA DMA ēĪdāŇŧā; IJæŽČāAŋ Streamā āAŎ ready āČŚāČČāČĤāČijāČĤāAŮāAęāĀAāČĜāČijāČĤāČŚāČzāČ■āČČāČŮāAŽāČŇæĤ' šēČ; āAŇ

2.3.2 āČňāČyāČzāČĚāzŤæġŸ

āČcāČĹ'āČňāČzāAřWISHBONEāAōāČřāČijāČĹ'āČcāČĹ'āČňāČzāĀČ āČňāČyāČzāČĚāzĚāČDāĹiæIJšāĀd'āAř pa-
rameter æŇGāōZāAğāđ'L'æZř'āRřēČ;āĀČ

2.3.3 āŇŤä;IJèĭñæŸŌ

CTL_CONTROL āAō bit0 āAŇ1āAōæŽČāAŋNæñqāĚČæġŇéĀāāAōāČGāČijāČĚāAōēĭāGžāAŮāČŠeāŇāAĎāĀAāRĎæñqāĚČāAōāĚ

CTL_CONTROL āAō bit2 āAŋ1āČŠčŋŇāAęāAĭāAĎéŽŘāČĹāAřāĀAçžřāČĹēŤāAŮāRŇāAYāŇŤä;IJāČŠeāŇāAęāĀČCTL_CONTROL
āAō bit2 āAŋ1āČŠčŋŇāAęāAšāāt'āRĹāAřæñqāZðèzcéĀAōNāžĚāAğāĀAbit0āAřēGĭāŇŤāČřāČĭāČcāAŤāČŇāAęāAJæāAŽāČŇāĀ

CTL_CONTROL āAō bit1 āČŠčŋŇāAęāČŇāAĹ1āAōæŽČāAřçžřāČĹēŤāAŮāAōāČĚāČđ'āČšāČšāČřāAğāČŠāČĹ'āČqāČijāČĚāAōāAęā
CTL_CONTROL āAō bit1 āAřēGĭāŇŤāČřāČĭāČcāAŤāČŇāČŇāĀČ āĚĚēČĹçŽĎāAŋāČŮāČcāČĹ'āČijāČňāČyāČzāČĚāČŠæIJL'āAŮāAęā

āL'sāČĹē;ijāAęāAřāĀA1āZđāAōèzcéĀAāAŇçŤČāČŘāČŇæřŌāAŋçŽžçŤšāAŽāČŇāĀČāČŠāČĹ'āČqāČijāČĚāAōæZř'æŮřāZĹçŤĎāČŠā

2.3.4 parameter ēĭāōŽ

āČGāČŤāČĹ'āČňāČĹāĀd'āAŇāČĹ'āđ'L'æZř'āAŽāČŇāĭĚēēAāAōāAČāČŇāRřēČ;æĀğāAōāAČāČŇāČČāAōāAāāAŠēĹŸēijL'āAŽāČŇā

2.3.5 āČĭāČijāČĹāzŤæġŸ

æIJñāČcāČyāČcēāČijāČŇāAōāČĭāČijāČĹāAōāRĎāĚqāRŮāAřäzēäyŇāAōēĀŽāČĹāĀČ

endian āAř āŇŤçŽĎāAŋāđ'L'æZř'āAŽāČŇāAšāAĹāAřæČšāōZāAŮāAęāAĎāAĭāAĎāAōāAğæšĭæĎRāĀČāČŘāČzāzĚāđ'L'æRZāAŇā;IJ

2.4 jelly_dma_stream_read

2.4.1 æęČèēA

(āAĹāAāāČGāČŘāČČāČřāyāĀAwriteāČČāČzāČČāČĹāAğēŮŇçŽžäy)

AXI4 āČqāČcāČĭāČŘāČzāAŇāČĹ'NæñqāĚČēĭāAğāGžāAŮāAę Stream āČŠāGžāĹZāAŽāČŇāĀČ
ēĭāGžāAŮāĚĹāAōāČŘāČČāČŤāČqāĹŮāqāAĹāĀAēĭāGžāAŮāČGāČijāČĚāAōāĹ'çŤĭāAř'āAĹāČŠçŇŋçŋŇāēĀġēŇŸāAŘæL'sāAĎāAđ

āĚĚēČĭāAŋFIFOāČŘāČČāČŤāČqāČŠæIJL'āAŮāAęāAĹāČĹāĀAāČŘāČČāČŤāČqæžcāČŇāAŮāAĭāAĎāĹēēGRāAōāAęāČŠēĭāAğāG

CPUāAŇāČĹ'āAōāČňāČyāČzāČĚçĹŮæĚŇāđ'L'āŇŮāČĎāĹ'sāČĹē;ijāAęāAřāĀAāĚĚēČĭFIFOāAōæōŇēGRāAŋçĎqēŮcāŤČāAŋAXI4ā
(āČGāČijāČĚāAōāōNāžĚāČŠçšēāČĹāAšāAĎāāt'āRĹāAřāĀAāČGāČijāČĚāČŠāĹ'çŤĭāAŽāČŇāAř'āAōāČšāČcāAŇāČĹ'āōNāžĚāĹ'sāČ

2.4.2 āČňāČyāČzāČĚāzŤæġŸ

āČcāČĹ'āČňāČzāAřWISHBONEāAōāČřāČijāČĹ'āČcāČĹ'āČňāČzāĀČ āČňāČyāČzāČĚāzĚāČDāĹiæIJšāĀd'āAř pa-
rameter æŇGāōZāAğāđ'L'æZř'āRřēČ;āĀČ

2.4.3 aNTä;JlèñæYÖ

CTL_CONTROL aAó bit0 aAñ1aAóæZĆaAñNæñqâĚĈæġNéĂăaAóăĈGăĈijăĈĚaAóĚl■ăĠzăAŮăĈSèqNăAĐăĂAăRĐæñqâĚĈăAóăĚ
 CTL_CONTROL aAó bit2 aAñ1ăĈSĉñNăAęăAłăAĐéŽŔăĈĹăAřăĂAçzřăĈĹèĚŤăAŮăRŇăAYăNTă;IJăĈSèqNăAĘăĂĈCTL_CONTROL
 aAó bit2 aAñ1ăĈSĉñNăAęăAşăât'ăŔĹăAřæñqâŽðèzcéĂAăŎNăžĚăAğăĂAbit0ăAřèĠăNTăĈřăĈĹăĈăAŤăĈNăAęăAĲă■ăăZăĈNăĂ
 CTL_CONTROL aAó bit1ăĈSĉñNăAęăĈNăAĲ1ăAóæZĆăAřçzřăĈĹèĚŤăAŮăAóăĈĚăĈđ'ăĈşăĈşăĈřăAğăĈSăĈĲ'ăĈqăĈijăĈĚăAóăAĚ
 CTL_CONTROL aAó bit1ăAřèĠăNTăĈřăĈĹăĈăAŤăĈNăĈNăĂĈăĚĚéĈĲçŽĐăAñăĈŮăĈăĈĹăĈĲăĈñăĈŷăĈZăĈĚăĈSăĲĲ'ăAŮăAęă
 âĹ'săĈĹèĲijăAĚăAřăĂA1ăŽđăAóèzcéĂăăAŇçĲĈăĈŔăĈNăřŎăAñçŽžçŤşăAŽăĈNăĂĈăĈSăĈĲ'ăĈqăĈijăĈĚăAóăZŤ'ăŮřăžĹçŤĐăĈSă

2.4.4 parameter èĲăăŎŽ

ăĈGăĈŤăĈĲ'ăĈñăĈĹăĂđ'aAŇăĈĹăđ'L'æZŤ'ăAŽăĈNăĲĚĚęAăAóăAĈăĈNăŔřĚĲ;æĂğăAóăAĈăĈNăĈĈăAóăAăăAŞĚĲĲ'ăAŽăĈNă

2.4.5ăĈĹăĈijăĈĹăžŤæġY

æIJăăĈăăĈŷăĈĚăĈijăĈñăAóăăĈĹăĈijăĈĹăAóăŔĐăĚqăŔŮăAřăžĚăŷNăAóĚĂŽăĈĹăĂĈ
 endianăAřăăNTçŽĐăAñăđ'L'æZŤ'ăAŽăĈNăAŞăAĲăAřăĈşăŏŽăAŮăAęăAĐăAłăAĐăAóăAğăşĲăĐŔăĂĈăĈŔăĈZăžĚăđ'L'æŔZăAŇă;IJ

2.5 jelly_dma_fifo

ăđ'ŮĚĈĹăĈqăĈĈăĈĹăĈSăĹĲçŤĲăAŮăAşăđ'ğăĈĲăĈđ'aĈZăAóFIFOăĈSăġNăĹŔăAŽăĈN

2.5.1ăĈñăĈŷăĈZăĈĚăžŤæġY

ăĈĈăĈĹăĈñăĈZăAřWISHBONEăAóăĈřăĈijăĈĹăĈĈăĈĹăĈñăĈZăĂĈăĈñăĈŷăĈZăĈĚăžĚăĈĐăĹăĲşăĂđ'aAř pa-
 rameter æŇĠăŏŽăAğăđ'L'æZŤ'ăŔřĚĲ;ăĂĈ

ăşžæIJñçŽĐăAñăĈqăĈĈăĈĹăĈSăĹ'săĈĹă;şăAęăAęăAŮăAĲăAĲăAřăĂAăĈZăĈĹăĈĹăĈijăĈăăĈGăĈijăĈĚăAñăřĲăAŮăAęăăĹăđ'ğăAĲĲ
 ăAşăAăăAŮăĂAăĈqăĈĈăĈĹăĈŔăĈZăžĚăAŇăĈZăĈĹăĈĹăĈijăĈăăĈŔăĈZăžĚăĈĹăĈĹăđ'ğăA■ăAĐăât'ăŔĹăĂAăĈŔăĈZăžĚăĹăAóăĈĈ
ăĈqăĈĈăĈĹl■ăAĲăæZŷăA■ăAóăŷqñřăAğăĈşăĈĈăĚĚăAñăĈĈăŔŔăAŤăAĲFIFOăĈSăĲăAăĈăAęăAĲăĈĹăĂAăĈGăĈijăĈĚăAóăŔĈăA
ăĈñăĈŷăĈZăĈĚăAñăAřăĈĚăĈđ'aĈăăĈĈăĈĚăĈĹăĈñăĈŷăĈZăĈĚăĈSçŤĲăĐŔăAŮăAęăAĲăĈĹăĂAăĈGăĈijăĈĚăĈĐç'žăA■ăAŇ
awlen/wrlenăAóăĈĲăĈđ'aĈZăŔĈăĈŔăAłăAŔăAęăĈĈăĈĚăĈđ'aĈăăĈĈăĈĚăĈĹăAŽăĈNăAřðèzcéĂăăĈSèqNăAĘăĂĈăĈĚăĈđ'aĈăăĈĈă

2.6 jelly_dma_video_write

AXI4	Stream	Video	æŽŷăA■ĲijăAĲçŤĲăAóDMA	jelly_dma_stream_write	aAó	N=3
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ăAóăĈŤ'ăĈĈăĈSăĈijăAĲăAŮăAęăăŏşĚĚăAŤăĈNăAęăAĐăĈN

2.6.1 ĀČňāĆÿāĆžāĆ£āžŦæġŸ

āĆćāČĹ'āČňāĆžāĀřWISHBONEāĀőāČřāČijāČĹ'āĆćāČĹ'āČňāĆžāĀĆ āČňāĆÿāĆžāĆ£āžĚāČĎāĹīæIJšāĀđ'āĀř pa-
rameter æŊĠāőŽāĀğāđ'L'æŽř'āŘřèČ;āĀĆ

2.7 jelly_dma_video_read

AXI4 Stream Video ěĤāĀŁāĠžāĀŮčŦíāĀđDMA

CHAPTER 3

Video éÚcéĂč

/rtl/video äzëäyÑāAñāAČăČŇăČăCŷăČěăČijăČń

3.1 jelly_video_parameter_update

3.1.1 æęCèęA

frame start āČŠæd' IJăGžăAŮăAęăČŠăČl' āČăăČijăČĚăŽt' æŮřăĚăăRŭăăČŠăăGžăAŽ

3.1.2 āČňăČŷăČzăČĚăžTæğŸ

āČăăČl' āČňăČzăAřWISHBONEăAğăăČřăăČijăČl' āČăăČl' āČňăČzăĂČ āČňăČŷăČzăČĚăžĚăČĎăĹăIJşăăĂd'ăAř pa-
rameter æŇĞăăŽăăAğăăL'ăŽt'ăŘřêČ;ăĂČ

3.1.3 âŇŤăĴJèĴñæŸŎ

ăŘĎçŤzăăČŖăăĞęçŖĚăČşăăČăăAğăăĞęçŖĚăĂŤăŷ■ăAñăăČŠăăČl' āČăăČijăČĚăăAŇăăd' L'ăăŇŮăăAŽăăČŇăăAĴăăŷ■ăŤřăăŘĹăăČŞęŭăăAŞăăAŽăăČşăăČ
æIJňăăČşăăČăăAřăăĂăăAŤăăČl'ăăAřçŤzăăČŖăăĞęçŖĚăăČşăăăČăăăŮşăăăAğăăŷ■ăŤřăăŘĹăăČŞęŭăăAŞăăAŤăăAĴăăăDăăăAşăăăČăăăAñăăăĂăăăĴŇçŭŹăăăČşăăăČ

3.2 jelly_video_format_regularizer

3.2.1 æḡĊèḡA

AXI4-Stream Video āĊŠæŃĠǫŽāĊŦāĊŦ' āĊijāĊđāĊĊāĊĹāAñæ■ċèḡRāŃŨāAŽāĊŃ

3.2.2 āĊñāĊŸāĊzāĊ£āzŦæḡŸ

āĊċāĊĹ' āĊñāĊzāAŦWISHBONEāAǫāĊŦāĊŦāĊĹ' āĊċāĊĹ' āĊñāĊzāĊĊ āĊñāĊŸāĊzāĊ£āzĒāĊĐāĹiæIJšāĊđ' āAŦ pa-
rameter æŃĠǫŽāAǫāđ' L'æŽŦ' āŦŦèĊ; āĊĊ

3.2.3 āŦŦāŦIJèlñæŸŌ

äŸzāAñċŦzāĊŦāĊĒēāĹZāĊĠāĊŦāĊđ' āĊzāAǫċŦAċĐŦāAǫāĹĠæŨ■āĊĐāĊĠāĊŦāĊċæñāāAšāĊāāĊŦāĊŦ' āĊijāĊđāĊĊāĊĹäŸ■äŸĒèĠ' ā

3.3 jelly_vin_axi4s

āĊŠāĊĠāĊĹāĊĒēāĹZāĊĊ AXI4 Stream video āAñāđ' L'æŦŽ

3.4 jelly_vout_axi4s

AXI4 Stream video āĊŠāĊŠāĊĠāĊĠāĊŽāĹZāAñāđ' L'æŦŽ

3.5 jelly_vsync_generator

āĊŠāĊĠāĊĠāĊŽāĹZċŦŦāAǫāŦŦæIJšāċqāŦŦċŦšæĹŦ

3.6 jelly_dvi_tx

DVIāĠžāĹZāĊšāĊĊ

3.7 jelly_mipi_csi2_rx

MIPI-CSI2 āAǫāŦŦŦāċqāĊšāĊĊ

CTL_CONTROL āAō bit0 āAŅ1āAōāēZĊāAŋçṬzāČRāČĜāČijaČfāAōē1āGzāAŮāČSēqNāAĚāĀČCTL_CONTROL
āAō bit2 āAŅ1āAōāāṭāRLāAġāĀPARAM_SIZEāLĚēzčēĀAāAZāČNāAĹāAIJæ■ēāZzāČN(bit0āAġēĜlāNŤzāČrāČlāČć)āĀČ
āAĹāAĚāAġāAĹāAĎāāṭāRLāAġçṬzāČRāAŅāĹēāČNāAšāAšāAŋçzrāČLēTāAŮēzčēĀAāČSēqNāAĚāĀČ
CTL_CONTROL āAō bit1 āAŅ1āAōāēZĊāAġēzčēĀAēŮāġNāČDčzrāČLēTāAŮāAōāČfāČd'āČšāČšāČrāAġāČSāČr'āČqāČijaČfāAŅ
PARAM_SIZE āAġ PARAM_WIDTHĀŮPARAM_HEIGHT āAōāČĹāČd'āČzāAġāAČāČNāĹēēēAāAŅāAČāČNāĀČ

çŤžǎČŘǎĞęçŘĘéŮcéĂč

4.1 img ãČŘãĆźäzŤæğŸ

[illegible]

4.2 jelly_axi4s_to_img

AXI4 Stream $\tilde{a}\check{C}\check{S}$ img $\tilde{a}\check{C}\check{R}\check{a}\check{C}\check{z}\check{a}\check{A}\check{n}\check{a}\check{d}'L'\check{a}\check{e}\check{R}\check{Z}\check{a}\check{A}\check{U}\check{a}\check{A}\check{A}\check{C}\check{T}\check{z}\check{a}\check{C}\check{R}\check{a}\check{G}\check{e}\check{C}\check{R}\check{E}\check{a}\check{C}\check{N}\check{a}\check{A}\check{n}\check{a}\check{E}\check{a}\check{A}\check{S}$ AXI4 Stream

4.3 jelly_img_blk_buffer

çTzâČŘăĈŠ NĀŪM āAőăĈŪăĈ■ăĈĈăĈřăĀñăđ'L'æRZăAŪăAęçTzâČŘăĠęçŘĒăĈŠăĈĉăĈăăĈzăĈĹăĂZăĈNăĈşăĈĉăĂĈăĈřăĈđ'ăĈşăĈŘăĈĈăĈĈTăĈăăĀĹăĈĈăĈřăĈzăĈñăĈŘăĈĈăĈĈTăĈăăĈŠăĈZăĀĹăĂĂăĈŪăĈ■ăĈĈăĈřăĠęçŘĒăĀñăđ'ĒĒęĀăĀĹă■Ÿă;■ăĂğM-1 āĈřăĈđ'ăĈşăĹĒăĂőăĀĒăžăűăĀŊçŽzçTşăĂZăĈNăĂĈ

ăĀĹăĂőăŽZăĂĂăĈĪĹăĈĭĵăĈĂăĈĭĵăĠęçŘĒăĀĹăAŪăAę NONE, CONSTANT, REPLICATE, RE-
FLECT, REFLECT_101 āĀĹăĀĹ'ăĂőăĠęçŘĒăĀŊăĀŸăĹđăĹřĒç;ăĂğăĂĈăĈĹăĂĂOpenCV
ăĂőăĀĹăĈNăĀĹăĂzăĀĭĵăŸĂĒĠř'ăĂZăĈNăĂĈ

4.4 jelly_img_demosaic_acpi

ACPIæşTăĀñăĈĹăĈNăĈĠăĈĈăĈăăĈăăĈđ'ăĈř

4.5 jelly_img_color_matrix

ăĈñăĈř'ăĈĭĵăĈđăĈĹăĈĹăĈĈăĈăĈřăĈzăĠęçŘĒ

4.6 jelly_img_gaussian_3x3

3x3 éZŘăőŽăĂőăĈñăĈęăĈăăĈăăĈşăĈĈăĈĈăĈñăĈĹ

4.7 jelly_img_sobel_core

Sobel āĈTăĈĈăĈñăĈĹăĈşăĈĉăZăăđ'ăĂğăĂőăĭĵTçőŪăĀĹăĂőăĂğăĈZăĈzăĈĹăĀŊăĈĹ'ēĹ■ăőŽăĂZăĈNăĈñăĈŸăĈzăĈĹăĂřăĀĹăĂ

4.8 jelly_img_binarizer

ĭĭĵŠăĂđ'ăŊŪăĈşăĈĉă

4.9 jelly_img_selector

çTzâČŘăĈzăĈñăĈřăĈĹăĂĈăŸzăĀñăĈĠăĈŘăĈĈăĈřăĈZăĈăĀñăŸ■éŪşăĈĠăĈĭĵăĈĹăĈŠăĹĠăĈĹăĈZăĹăĂĹăĂęăĠăĹăĂZăĈNăĀĹăĀĹ'ă

4.10 jelly_img_previous_frame

lãAđ' ãL■ãAôãČTãČňãČijaČããČŠãđ' ŮéČlãČqãČćãČlãAňãŁlã■ŸãAŮãAęãLl'çTlãAŽãČNãAșãČAãAôDMAãČćãČyãČěãČijaČň

4.11 jelly_img_dnn_maxpol

DNNçTlãAô MaxPooling åsd'ãĂĆ

çÿôârRãČŠëãNãAĐ de ãAôçñNãAșãAłãAĐãČGãČijaČŁãČŠãGzãŁZãAŽãČNãAșãČAãĂAãAșãAôã;NãAňãAŤãČL'ãAňçTzãČRãGęçŁ
AXI4 Stream ãAňæŁzãAŽãŁEëçAãAňãĂČãČNãĂĆ

/r/|library äzëäyNāAñāĀAāĈLāARā;£āAĒāĈl'āCđ'āČŮāĈl'āĈiçŽĐāAĥāĈcāĈyāĈcāĈijāĈñç;đ'āĈŠēZĒāĈAāAĕāAđDāĈNāĈĀ
āAĥāĈNāAžāARāyNēlŸāAōāĒ■æT'rĈĒēAŮāĈLāAĒēAĀlāAŮāAĕāAđDāĈNāNāĀAĕR;çŁūāA;āAāāRđ'āAđDāĈĈāAōāĈĈæuūāIJāAŮ

åEËèTṭãČąãČcãČłéŮcéĂcãĄóãČř'ăĆd'ăČŮăČř'ăČłçłd'

FIFOãĈŠæĝŊæĹŔãȦŽãĈŊãĈĹ'ãĈd'ãĈŮãĈĹ'ãĈĭçĭd'

āAđ' āAġāAŃāČŁāĆŚæŅāāAšāAġāAĐā■ŸçŻżāAōāČGăCġjāČĚāAňár;āAŮāAęāGeçŘEāČŚëāŅāAĚāČř'āČď'āčŮāčř'āčĹčğ'd'
āşžæIŋčŽDăAn AXIăČŘăČzæŮžajįRăAő valid/ready æŮžajįRăAğăČŘăČśăČĹ'ăčŭăčgăCġjāČřăAZăČŇ

5.2.4 jelly_stream_XXX çşž

last āČŦāČŦ' āČřāAļāAļ' āAğāNžāĽGāCL' āČŇāČŇāÿĂéĂčāAđāČzāČĽāČĽāČijāČāāČGāČijāČĤāAñāŕĴ āAŮāAęāGęçŘĚāČŠèāŇāAĚāČŦ' āĴāđIjāÿĽāČzāČĽāČĽāČijāČāāĚĽā■āČŠèāĽāAŽ first āČŠāžŸāAŠāČĽ' āČŇāČŇāČČāAđāČČāAČāČŇāĂČ āšžæIJñçŽĎāAñ AXIāČŘāČzæŮžāijRāAđ valid/ready æŮžāijRāAğāČŘāČšāČĽ' āČŮāČgāČijāČřāAŽāČŇ

5.2.5 jelly_address_XXX çşž

āŘĎčĽāČčāČĽ' āČňāČČāČŮāČšāČřāAđçČzāAđāČđ' āČšāČGāČČāČřāČžçŦšæĽŘāČ■āČÿāČČāČř

5.2.6 jelly_capacity_XXX çşž

āŘĎčĽāđāđzéĞŘçđaçŘĚāČňāČęāČšāČĤ

5.2.7 jelly_func_XXX çşž

çŦĎāAĤāŘĽāČŘāAŽāZđèŮŕāAñāČĽāČŇāŘĎčĽāČŦāČqāČšāČřāČŮāČgāČšæŘŘāĴ

5.2.8 āAļāAđāžŮ

āAļāAđāžŮāAĎāČ■āAĎāČ■

CPU (MIPS-I compatible)

6.1 æøĈèøÅ

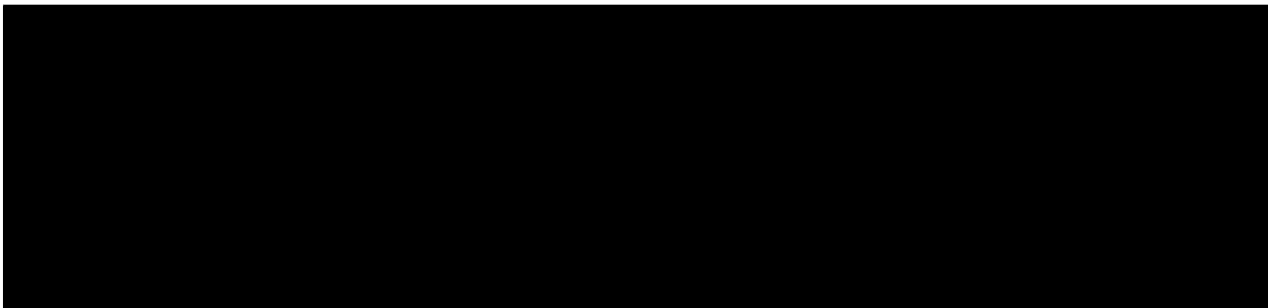
Jelly āĀlāĀfāĀĀFPGAāĀSāĀSāĀō MIPS-I āĈfāĈd'āĈfāĀĤāS;āzd'āĈzāĈĈāĈĤāĀōāĈsāĈcāĈSæIJL'āĀŪāĀsāĈ;āĈTāĈĤāĈsāĈcāĈŪāĈāĈzāĈĈāĈŪāĈsāĈfāĈāĈzāĈĈāĈāĀgāĀZāĈ

6.2 CPUāĈsāĈcāĀōæøĈèøÅ

Jelly āĀōāĈZāĀĤāĈN CPU āĈsāĈcāĀfāS;āzd'āĈzāĈĈāĈĤāĀlāĀŪāĀøāĀfāĀ MIPS-I āĈfāĈd'āĈfāĀĤāS;āzd'āĈzāĈĈāĈĤāĈS āĈZāĀĤāĀøāĀĤāĈĤāĀgcc āĀĤāĀĤ'āĀnāĈĤāĈNéŪNçZzāĀNāRfēĈ;āĀgāĀZāĈ āĀsāĀāāĀŪāĀĤwl, lwr, swl, swr āS;āzd'āĀfāĈZāĀĤāĀøāĀĎāĤāĀĎāĀōāĀgæslæĎRāĀNāĤĒøøĀāĀgāĀZāĈ āĤ'sēçijāĀĤāĤāĤ'āĀōāS;āzd'āĈzāĈĈāĈĤāzēād'ŪāĀōæĤ'sēĈ;āĀfāšzæIJñçZĎāĀñçNñèĤlāzTægYāĀĤāĀōāĀgāĀĈāĀRāĀçāĀg āĈsāĈsāĈSāĈd'āĈfāĀNætĀçTlāĀgāĀāĈNāĀĤāĀĎāĀĖāzēād'ŪāĀfMIPSāĈŪāĈzāĈĖāĈāāĤlāzSæRZæĀgāĀfāĀĈāĈĤāĀçāĀZāĈSā

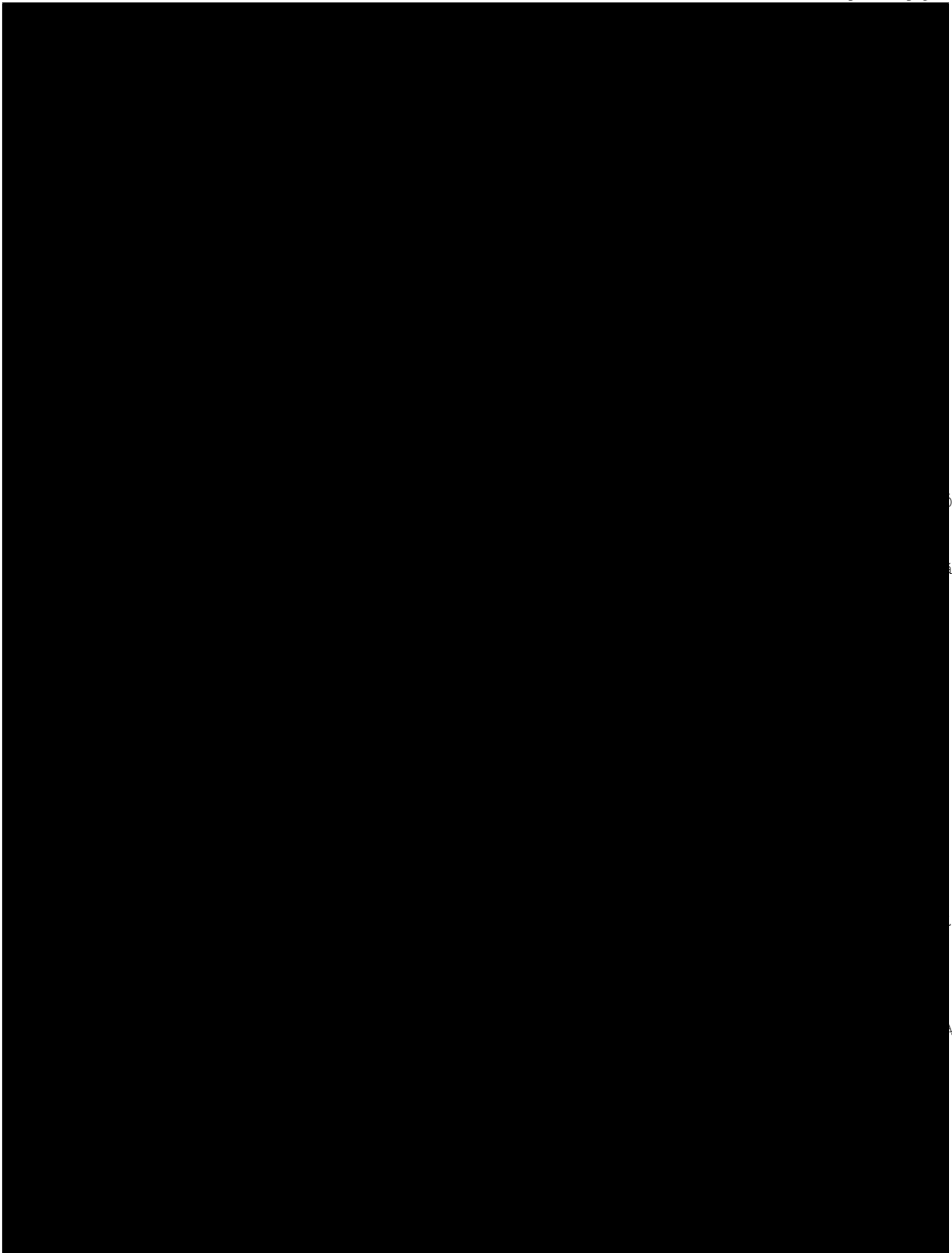
6.3 jelly_cpu_top āĈcāĈyāĈēāĈijāĈn

6.3.1 āĈSāĈfāĈçāĈijāĈç



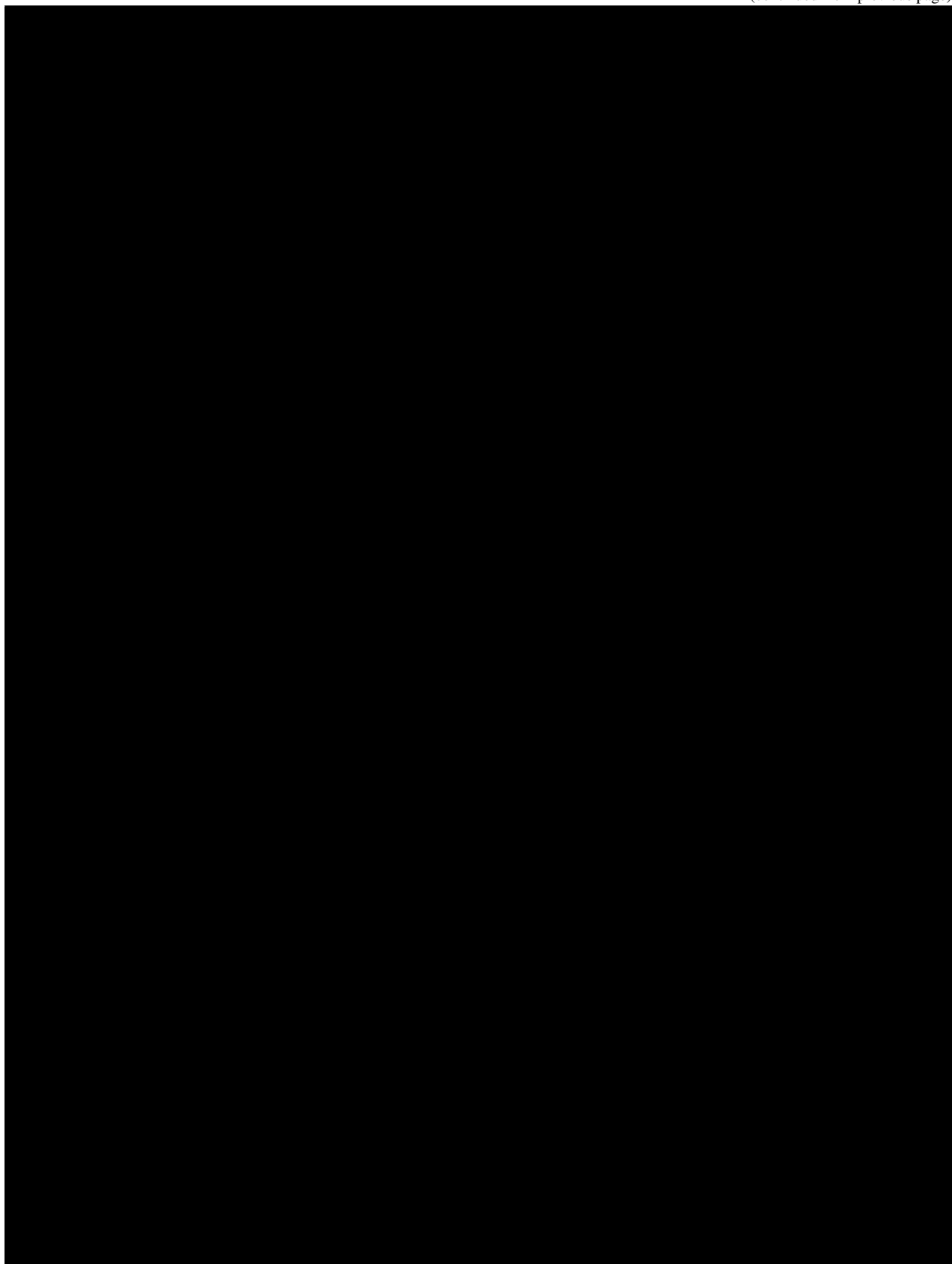
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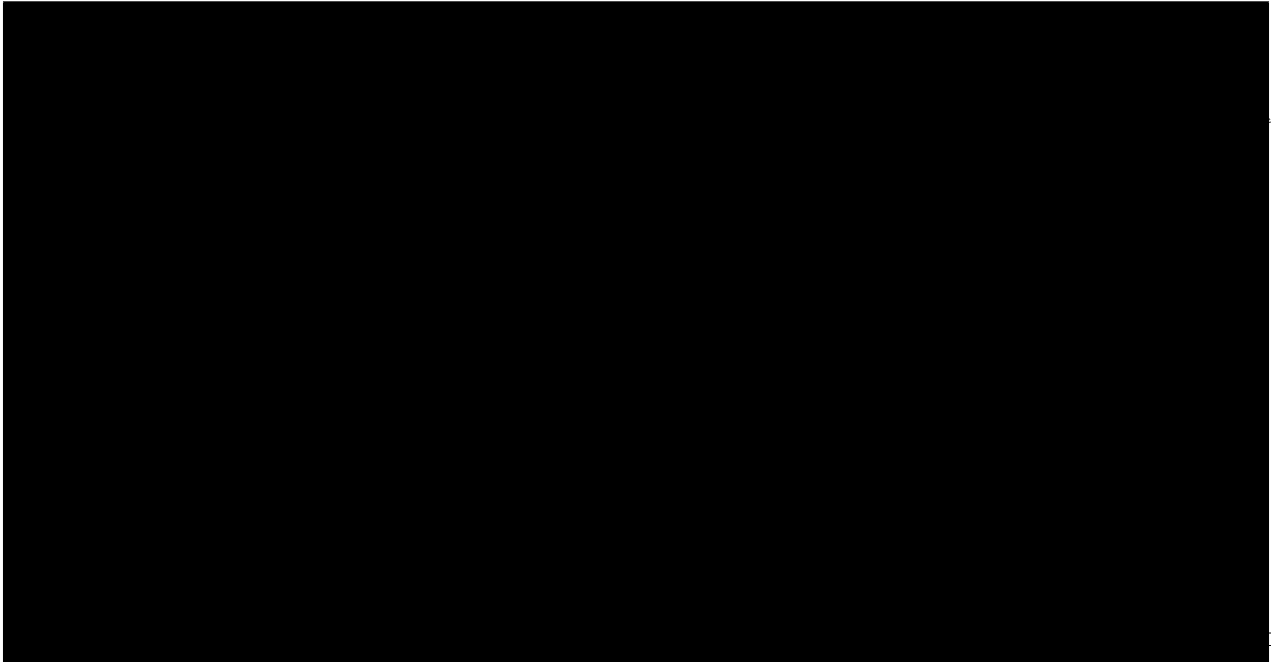
(continues on next page)

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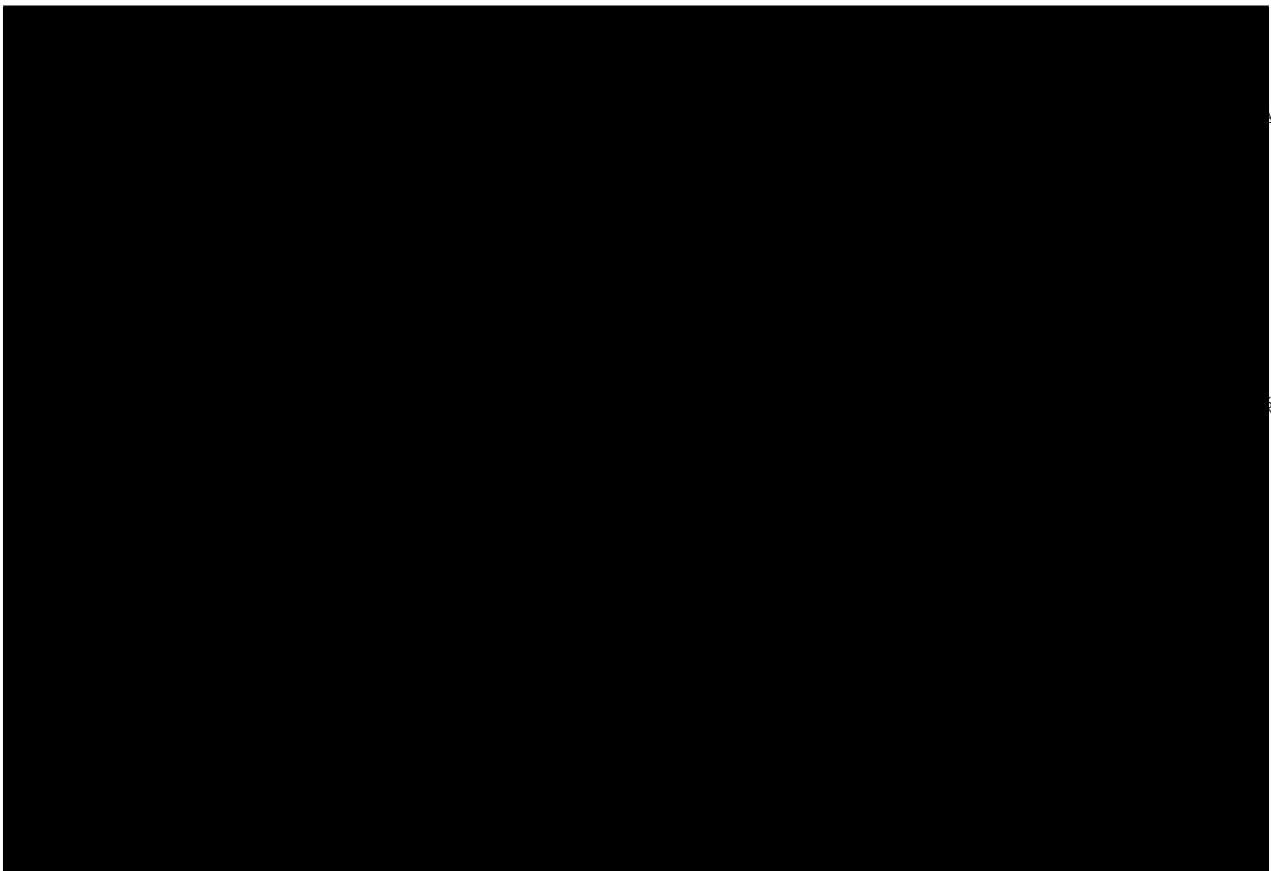


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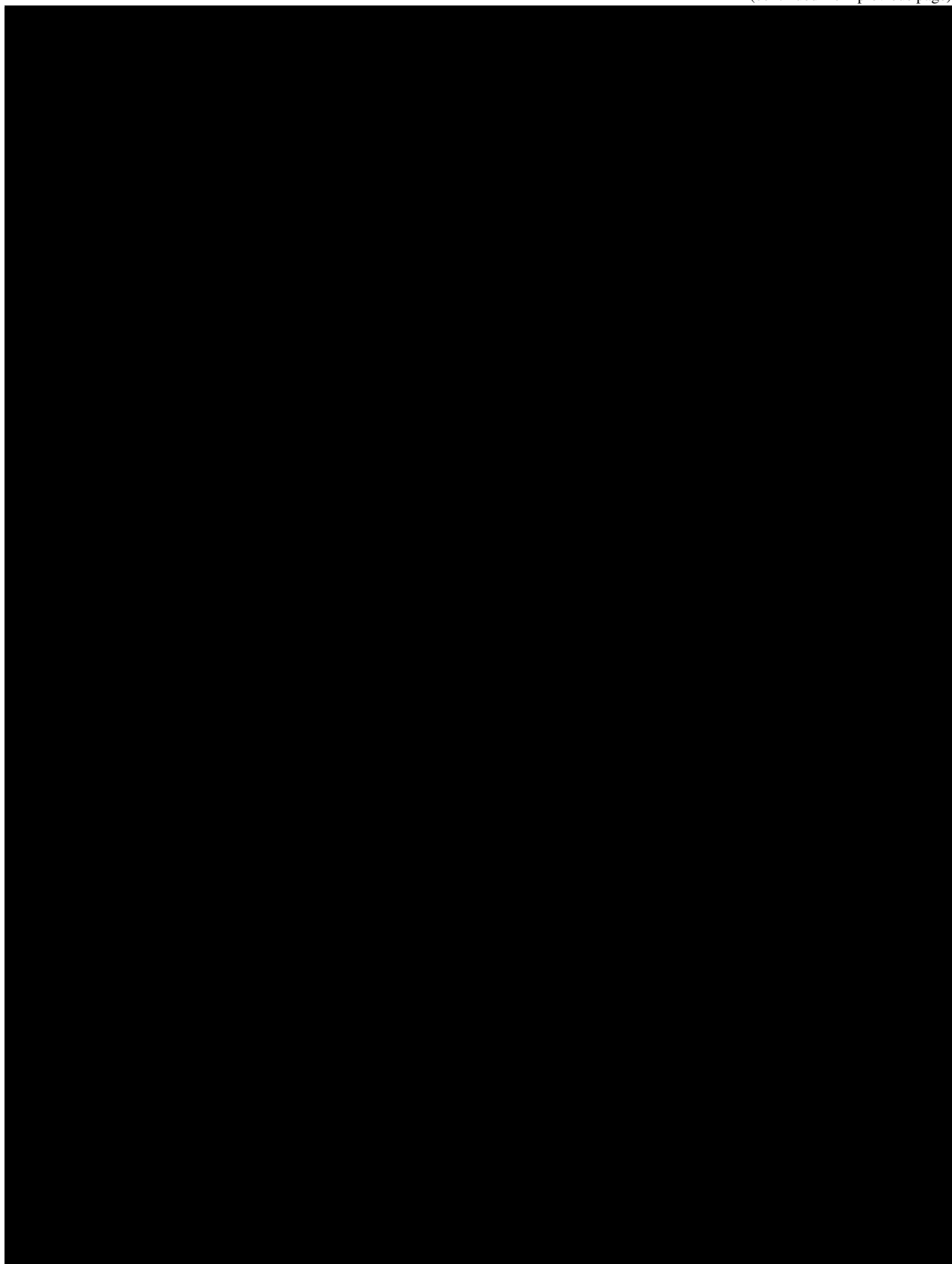


6.3.2 ĀČlāČijāČLäᄁāŕũ



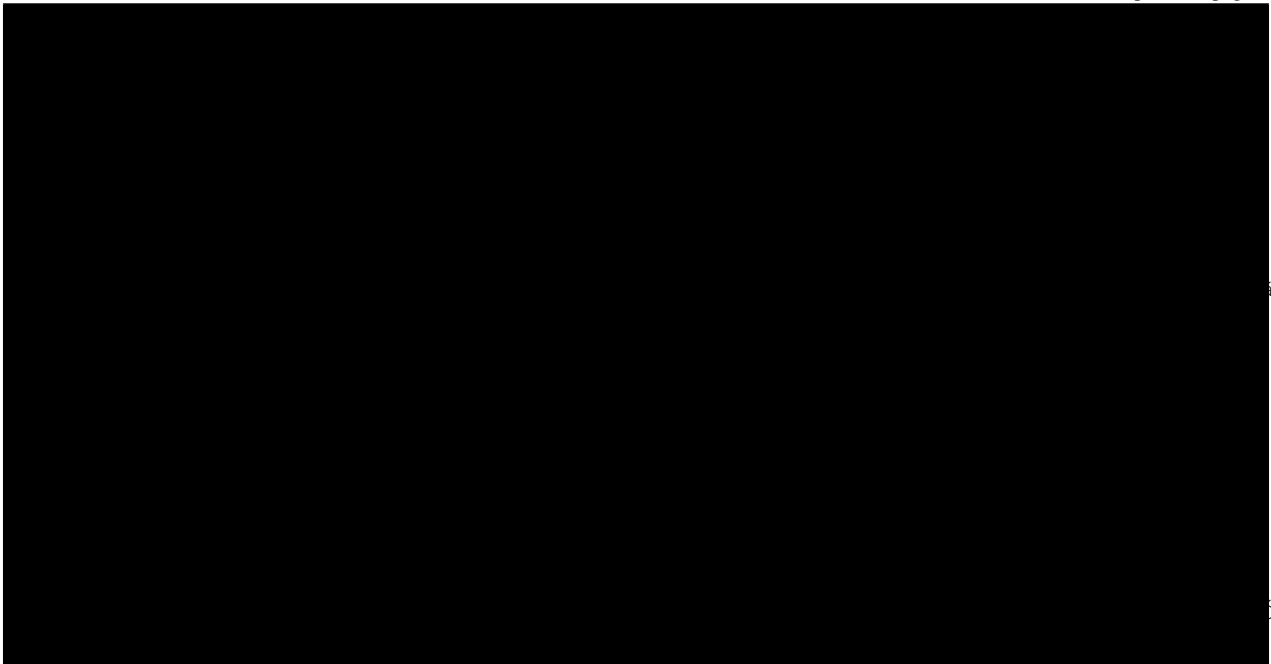
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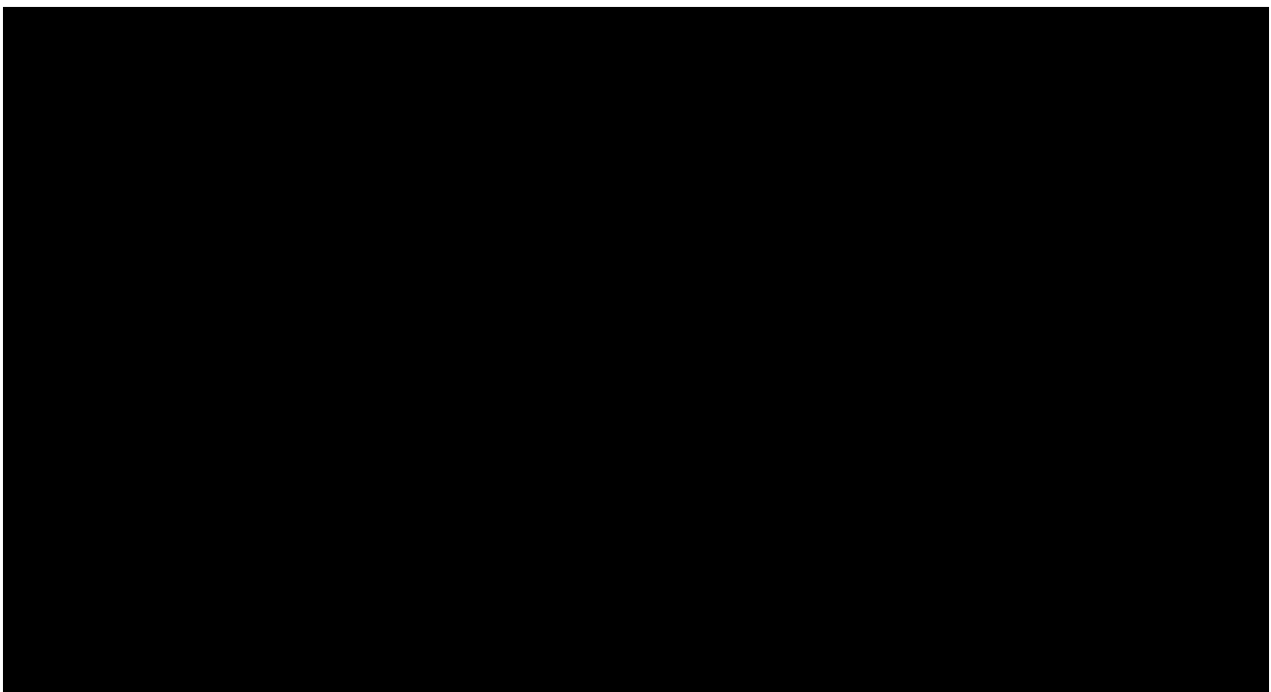
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6.4 CPUāČĜāČŘāČČāČřāČlāČijāČĹ

āČĜāČŘāČČāČřāČlāČijāČĹāAŁNaČĹ'āyNēlŸāAŁāČāČĹ'āČnāČzāAŁnāČāČřāČzāČzāAŁģāAŁāAŁāZāĀČ

6.4.1 āČnāČŸāČzāČŁāzŢæġŸ

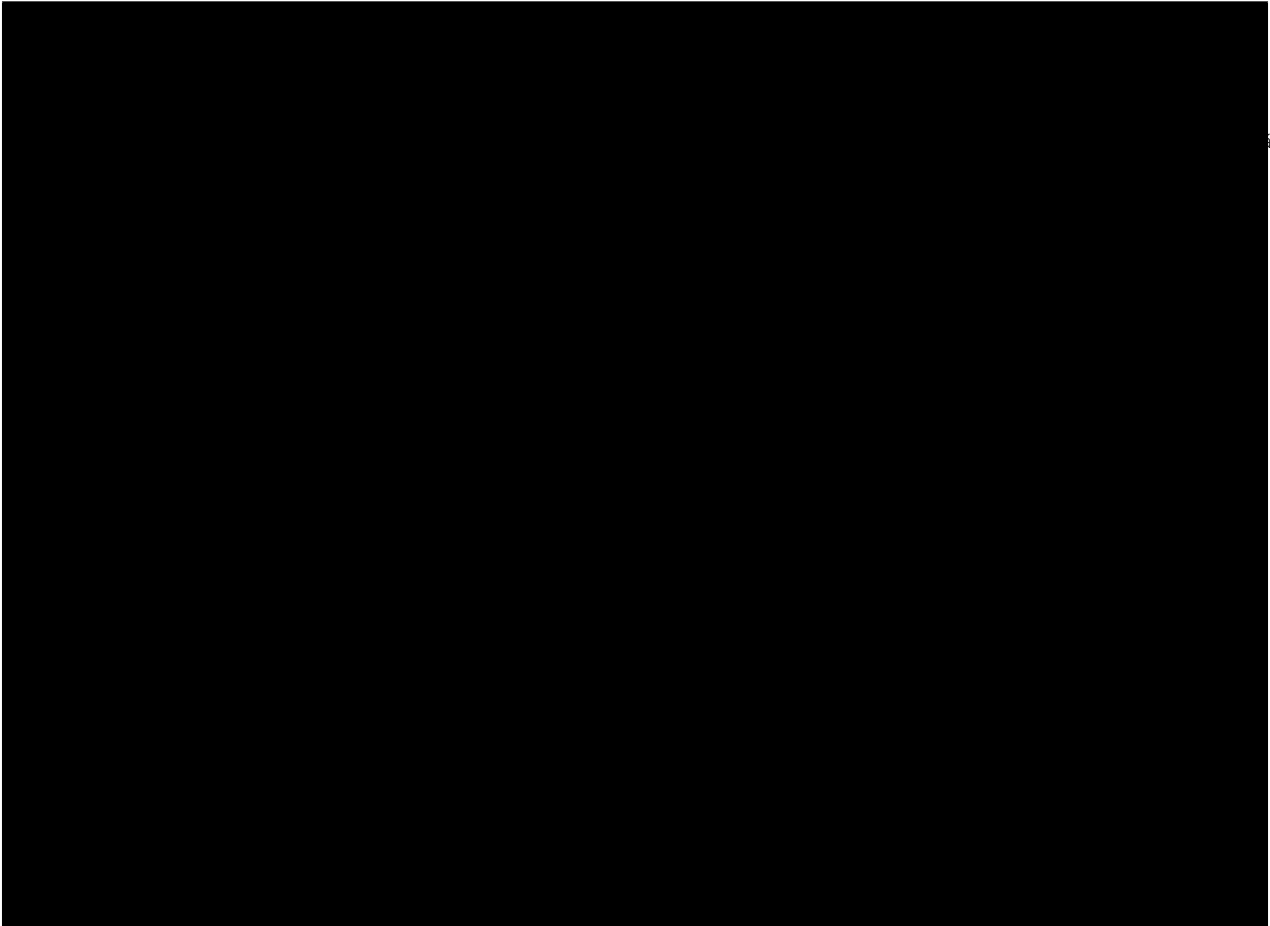


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1. The first step in the process is to identify the problem or issue that needs to be addressed. This involves gathering information and understanding the context of the problem.

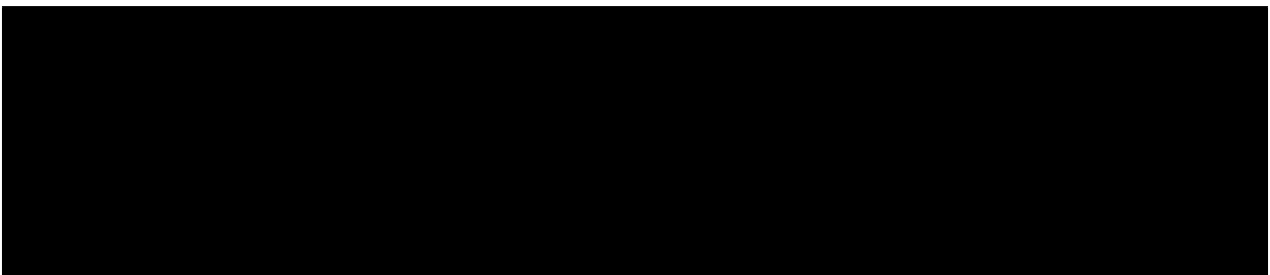
6.6 WISHBONE

Jelly OpenCores(<http://www.opencores.org/>) WISHBONE
 WISHBONE is a hardware description language (HDL) for the WISHBONE bus. It is a subset of Verilog and is designed to be easy to use and to be portable across different hardware architectures. The WISHBONE bus is a simple, efficient, and flexible bus architecture that is suitable for a wide range of applications. It is designed to be easy to use and to be portable across different hardware architectures. The WISHBONE bus is a simple, efficient, and flexible bus architecture that is suitable for a wide range of applications. It is designed to be easy to use and to be portable across different hardware architectures.



6.7 Jelly bus

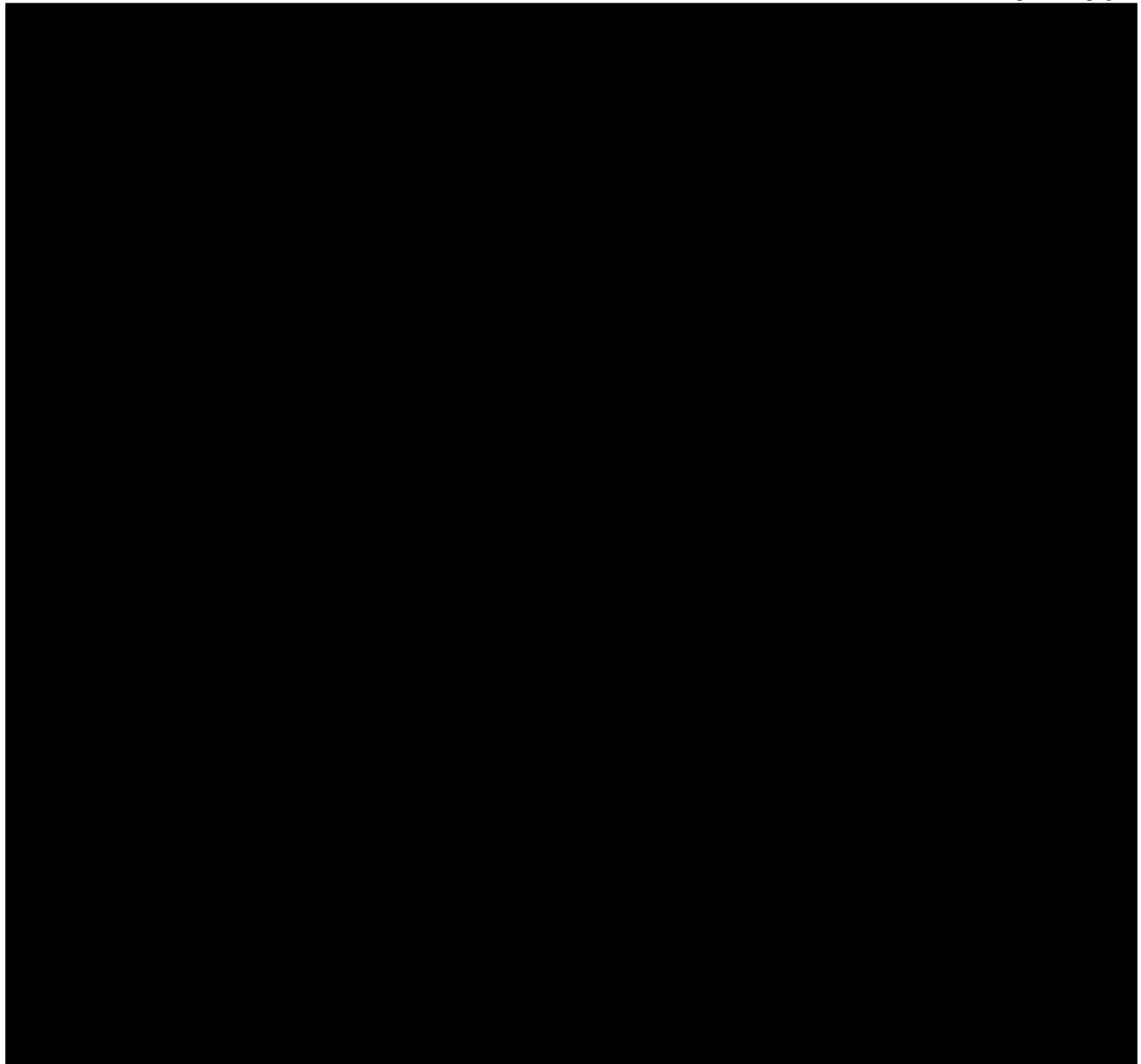
WISHBONE is a hardware description language (HDL) for the WISHBONE bus. It is a subset of Verilog and is designed to be easy to use and to be portable across different hardware architectures. The WISHBONE bus is a simple, efficient, and flexible bus architecture that is suitable for a wide range of applications. It is designed to be easy to use and to be portable across different hardware architectures. The WISHBONE bus is a simple, efficient, and flexible bus architecture that is suitable for a wide range of applications. It is designed to be easy to use and to be portable across different hardware architectures.



→ WISHBONE is a hardware description language (HDL) for the WISHBONE bus. It is a subset of Verilog and is designed to be easy to use and to be portable across different hardware architectures. The WISHBONE bus is a simple, efficient, and flexible bus architecture that is suitable for a wide range of applications. It is designed to be easy to use and to be portable across different hardware architectures.

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6.8 ěŠĵăzd'ăĈzăĈĈăĈĹĉ■L'

ăAŞăAqăĈL'ăAŋçĵăăAĎăAęăAŁăA■ăAĭăAŻăĂĈ
files/jelly_cpu.ods

MIPS-I ažŠæRŽãĈĵãĈĹãĈşãĈćĉŠřácĈ

7.1 ãAřãAŸãĈAãAń

MicroBlazeãAŇĈDqãDşãAğãL'çTĹãAğãA■ãĈZynqãAńARMãĈşãĈćãAŇæR■ëijL'ãAŤãĈŇãĈAãzLãAĴãAĴãAĈãAęãAřéAŮãŮãAóé
 Jelly ãĈũãĈzãĈĚãĈãĈŞãĴIãĈĹãğNãĈAãAşãA■ãĈãĈAŇãAşãAřãĈŇCPUãĈŞæZÿãAĎãAęãAĚãAşãAĎãA■ãAŇĈZžĉńřãAããĈãAş
 rtl/cpu ãĈŤãĈĹ'ãĈńãĈĈãAóãÿNãAńã;ŞæZĈRTLãĈŞëŽãAĹãAşãAřãAŇãĈĹãAóéãĈãAńæZÿãAĎãAşCPUãAóRTLãĈşãĈĵãĈĹ'ãAŇã
 HTML ãAğæZÿãAĎãAşëñæŸŮæŮGãĈŞãzŁéćĹãAń Markdown ãAńæZÿãA■ĈŽĹ'ãAŮãAęãAĚãAşãAĎãAĴãĈĹãAĴãĎãAĴãAŽãĈ

7.2 Jelly-CPUāAőçL'žāžt'

- MIPS-IāČl'āČd'āČrāAłāS;āzd'āČzāČČāČLāAğāĀAgcc āAğCēlĀēlđēŨŇçŽžāAŇēāNāAŁāČN
- āČ■āČčāČČāČūāČēāf'šēČ;
- āfEçłRāRĹLāČqāČčāČl(TCM)āf'šēČ;
- Verilog 2001 āAğēlŸēfř
- DDR-SDRAMāf;āŁIJ
- ĪijITRON4.0āžTāğŸ HOS-V4a āf;āŁIJāy■
- ICEāŇāijçŦīāAōāČGāČRāČČāČrāČlāČijaČLāČŠāČŽāAŁāAęāAĐāČN
- āČ■āČčāČqāČšāČLāAŇāŮēāIJñēlđ(ā;IJēĀĒāAŇēNšēlđāČRāAŇāČšāAłāAD)

7.3 āČūāČžāČĚāČāāAőèłŋæŸŮ

7.4 CPUāČšāČčāAōāČŮāČ■āČČāČrāŽš

āĒyāđŇçŽĐāAłāT'ZçğSæŽyāAł'āAŁāČLijTāōłāČŠāČd'āČŮāČl'āČd'āČšāAōāČčāČijaČ■āČĚāČrāČAāČčāAğāĀAāČšāČčēČlāAłāČRā
āA;āAšāĀAāĒĚēŦīāAğāČGāČRāČČāČrāČēāČNāČČāČLāČČçŦīāĐRāAŮāAęāAŁāČLāĀAāđ' ŮēČlāAŇāČL'CPUāČšāČčāČšçŽt'æŮ
āČŦāČl'āČrāČijaČGāČčāČšāČrāČČāĒēāAčāAęāAŁāČLāĀAāōĒāAł'āAōāōšēāNāČēāČNāČČāČLāČSEXāČžāČĚāČijaČyāAŇāŽžāČA
lāAğāAłāČ■āČijaČL'āS;āzd'āAōāŇāāS;āzd'āAğāAłāČ■āČijaČL'āAŮāAšāČŇāČyāČžāČčāAłāŁl'çŦīçAęā■āAğāŽāAōāAğāĀAçL'žā
āAłāAōāžŮāĀFPGAçŽĐāAłāūēāđ'ŇçČžāAłāAŮāAęāAłāĀAāČŦāČl'āČrāČijaČGāČčāČšāČrāAōāČđāČŇāČAāČŮāČŇāČrāČłāAŇFP
mflō)āAğāžŮçŮŮāAŽāČŇāČLāAĒāAŇāAŮāAęāĀAēēNāAŇāAŠāyLāĀAāžŮçŮŮāAłl'āČłāČd'āČrāČŇāAğāōšçR;āAğāA■āA;āAŮāA
āČGāČRāČČāČrāČēāČNāČČāČLāAōāČLāAĒāAłāČIJāČLāČŇāČ■āČČāČrāČšāČŇšāČšāAğ Spartan-3
āAōāČžāČŦāČijaČL'āČrāČŇāČijaČL'-4āAğ50MHzāAğāŇŦāAĐāAęāAĐāA;āAŽāAōāAğāA;āAČāA;āAČāAłāAĐāAčāAšāĀAšāČ■

7.4.1 CPUāČLāČČāČŮāAőèłŋæŸŮāžš

CPUāČšāČčāAōāđ' ŮāAŇāĀAāfEçłRāRĹLāČqāČčāČl(TCM)āAłāČ■āČčāČČāČūāČēāČšāēR■ēijL'āAŮāAęāAĐāA;āAŽāĀČTCMāAłāČ■
FPGAāAōāĒēēŦīāČGāČčāČčāČŇāČlāČijaČLāRAMāČŠēāNāAŇāAŮāAęāĀAīijŠāAđ'āAōāČlāČijaČLāČšāS;āzd'/āČGāČijaČčāAŇāŁ
āfEçłRāRĹLāČqāČčāČl(TCM)āČČFPGAāAōāĒēēŦīāČGāČčāČčāČŇāČlāČijaČLāRAMāAōāAŁāAŇāAšāAğāĀAāS;āzd'/āČGāČijaČčā

ãŘĎċłóãĈŘãĈzãAñãAřOPENCORES.ORG ãAő WISHBONEãĈŘãĈz ãAłãžŠæŘZæĂğãAñãAĈãĈNãĈŁãAĖãAñãIJãAċãAšãAđ'ãĈŁ

7.4.2 Spartan3e statrer kit ãAğãAőãĈuãĈzãĈĖãĈãAőøĭñæYŎãZş

ãžŁãŽđãAőãĈIJãĈijãĈŁ'ãAñãAřRS-232CãAññijŠãĈIãĈijãĈŁãAĈãĈNãAőãĈŠãAĎãAĎãAšãAłãAñãĂAãĈGãĈŘãĈĈãĈřãĈIãĈijãĈŁ
ãAĹãAšãĂAĖĭđãÿãAñãĈuãĈšãĈŮãĈñãAłãĈĈãAőãAğãAŽãAñDDR-SDRAMãAőãĹũã;ãRTLãĈĈæŽÿãAĎãAĖãAĤãAĹãAŮãAšãĈ
SDRAMãAř100MHzégĖãNŤãAłãAőãAğãĈřãĈ■ãĈĈãĈřãžŮãAŽãAñãAĹĸŤIãAőãĈŮãĈIãĈĈãĈÿãAñãĖãAċãAĖãAĎãAĹãAŽãĈ
ãĈŽãĈIãĈŤãĈğãĈĤ'ãĈñãĈŘãĈzãAñãAřãĎĎċłóãŠĖ;žãŽđèũřãĈŠãAđ'ãAłãAĎãAğãAĎãAĹãAŽãĈĈIRC(ãŁ'šãĈĹè;ijãAĤãĈšãĈĹãĈ

7.5 éŮŇçŽžçŠřácĈãAñãAđ'ãAĎãAĖ

7.5.1 ãĈŘãĈijãĈŁ'ãĈĖãĈğãĈċçŠřácĈ

éŮŇçŽžãĈIJãĈijãĈŁ'ãAñãAř Xilinx ãAő Spartan-3e Starter KitãĈŠã;ĤãAċãAĖãAĎãAĹãAŮãAšãĈ
ãžŁãAšãAłãĖãŁ'NãAřéŽãAŮãAĎãAłãĂIãAĎãAĹãAŽãAőãAğãĂAãAĈãAŘãAĹãAğãŘĈèĈĈãAğãAŽãĈ

7.5.2 ãĈĹãĈŤãĈŁãĈĖãĈğãĈċçŠřácĈ

Xilinx ãAő ISE WebPack ãĈŠã;ĤãAċãAĖãAĎãAĹãAŽãĈ

CelĬĖĭđãĈšãĈšãĈŠãĈđ'ãĈĤ'ãAřãĹšæŽĈ gcc ãAőãĈřãĈ■ãĈzãĈšãĈšãĈŠãĈđ'ãĈĤ'ãĈŠ cyg-
win ãÿŁãAğãğNĸřŁ'ãAŮãAĖã;ĤãAċãAĖãAĎãAĹãAŮãAšãĈzãŁãAšãAł Windows
æt'ĹãAőãžžãAğãĈĈ WSL ãĈĎ VirtualBox ãAñãAĈãĈŁãAĹãAŽãAőãAğ Linux
ãÿŁãAğãĈřãĈ■ãĈzãĈšãĈšãĈŠãĈđ'ãĈĤ'ãAñã;ĤãAĹãĈNãAłãĂIãAĎãAĹãAŽãĈ
swl,swr,lwl,lwr ç■Ł'ãAñã;ĤãAĹãAłãAĎãAőãAğãAłãAšãAããAšãAĹæřŮãĈŠãAđ'ãAšãAŘãAšãAŤãAĎãĈ

CHAPTER 8

Indices and tables

- `genindex`
- `modindex`
- `search`